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1 [A new buffer management scheme for hierarchical shared memory switches](#)

Abdul K. Choudhury, Eden L. Hahne

October 1997

IEEE/ ACM Transactions on Networking (TON) Volume 5 Issue 5

Publisher: IEEE Press

Full text available [PDF](#) (197.29 KB)

Bibliometrics Downloads (6 Weeks): , Downloads (12 Months): 6, Downloads (Overall): 336, Citation Count: 2

Keywords: asynchronous transfer mode, backpressure, buffer memories, hierarchical switch, losses, memory management, priorities, pushout, queuing analysis, shared memory systems2 [Xnet 2: lessons from an early wide-area ATM testbed](#)

Charles R. Salimades, Srinivasan Keshav, William T. Marshall, Samuel P. Morgan, Robert C. Beaulieu, Jil

February 1997

IEEE/ ACM Transactions on Networking (TON) Volume 5 Issue 1

Publisher: IEEE Press

Full text available [PDF](#) (231.69 KB)

Bibliometrics Downloads (6 Weeks): 1, Downloads (12 Months): 8, Downloads (Overall): 256, Citation Count: 0

Keywords: asynchronous transfer mode, available bit rate, constant bit rate, variable bit rate

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3 [Measuring the evolution of transport protocols in the Internet](#)

Alberto Medina, Mark Allman, Sally Floyd

April 2005

SIGCOMM Computer Communication Review Volume 35 Issue 2

Publisher: ACM

Full text available [PDF](#) (1.48 MB)

Bibliometrics Downloads (6 Weeks): 12, Downloads (12 Months): 79, Downloads (Overall): 1027, Citation Count:

In this paper we explore the evolution of both the Internet's most heavily used transport protocol, TCP, and the current network environment with respect to how the network's evolution ultimately impacts end-to-end protocol. The traditional end-to-end...

Keywords: Internet, TCP, evolution, middleboxes4 [Communications of the ACM: Volume 52 Issue 7](#)

July 2009

Communications of the ACM

Publisher: ACM

Full text available [Digital Edition](#), [PDF](#) (7.61 MB)

Bibliometrics Downloads (6 Weeks): 128, Downloads (12 Months): 1194, Downloads (Overall): 1194, Citation Count:

5 [Communications of the ACM: Volume 52 Issue 12](#)

December 2009

Communications of the ACM

Publisher: ACM

Full text available [Digital Edition](#), [PDF](#) (7.37 MB)

Bibliometrics Downloads (6 Weeks): 133, Downloads (12 Months): 1142, Downloads (Overall): 1142, Citation Count:

6 [Proceedings of the 14th international conference on Principles of distributed systems](#)




Chengyang Lu, Toshimitsu Marukawa, Mohamed Mekkai

December 2010

OPDIS'10: Proceedings of the 14th international conference on Principles of distributed systems

Publisher: Springer-Verlag

Bibliometrics Downloads (6 Weeks): n/a, Downloads (12 Months): n/a, Downloads (Overall): n/a, Citation Count:

- 7 [Communications of the ACM: Volume 53 Issue 4](#)
 April 2010 Communications of the ACM
Publisher: ACM
 Full text available:  [Digital Edition](#) ,  Pdf (14.07 MB)
Bibliometrics: Downloads (6 Weeks): 209, Downloads (12 Months): 2046, Downloads (Overall): 2046, Citation Count: 0
- 8 [Modeling and evaluation of ring-based interconnects for Network-on-Chip](#)
 Stephan Bourdoux, Zeki Zili
 January 2011 **Journal of Systems Architecture: the EUROMICRO Journal** Volume 57 Issue 1
Publisher: Elsevier North-Holland, Inc.
Bibliometrics: Downloads (6 Weeks): n/a, Downloads (12 Months): n/a, Downloads (Overall): n/a, Citation Count: 0
 A popular network topology for Network-on-Chip (NoC) implementations is the two-dimensional mesh, which has drawbacks in the communication latency scalability, and the concentration of the traffic in the center of the mesh. In this paper, we consider
Keywords: Hierarchical ring, Interconnect topologies, Mesh, Modeling, Network-on-Chip, SystemC
- 9 [Design automation of real-life asynchronous devices and systems](#)
 Alexander Luchin, Boris Gotsman, Leonid Lavrov, Anna Rudnitsky, Andrey Pletzer
 January 2007 **Foundations and Trends® in Electronic Design Automation** Volume 2 Issue 1
Publisher: Now Publishers Inc.
Bibliometrics: Downloads (6 Weeks): n/a, Downloads (12 Months): n/a, Downloads (Overall): n/a, Citation Count: 0
 The number of gates on a chip is quickly growing toward and beyond the one billion mark. Keeping all the gates running at the beat of a single or a few rationally related clocks is becoming impossible. In static timing analysis process variations and ...
- 10 [In-network adaptation of video streams using network processors](#)
 Mohammad Shorrukman, Basil Bakir, Peter Graham
 January 2009 **Advances in Multimedia** Volume 2009
Publisher: Hindawi Publishing Corp.
 Full text available:  Pdf (4.60 MB)
Bibliometrics: Downloads (6 Weeks): 2, Downloads (12 Months): 18, Downloads (Overall): 42, Citation Count: 0
 The increasing variety of networks and end systems, especially wireless devices, pose new challenges in communication support for, particularly, multicast-based collaborative applications. In traditional multicasting, a sender transmits video at the ...
- 11 [CORNet: An OBS metro ring network with QoS support and fairness control](#)
 Huo-Feng Lu, Wang-Feng Chang
 July 2008 **Computer Networks: The International Journal of Computer and Telecommunications Networking** Volume 52 Issue 10
Publisher: Elsevier North-Holland, Inc.
Bibliometrics: Downloads (6 Weeks): n/a, Downloads (12 Months): n/a, Downloads (Overall): n/a, Citation Count: 0
 The rapid evolution of networks has been driven by new advances in enabling technologies and the explosive growth of Internet traffic. Optical Burst Switching (OBS) is not only cost-effective, but also provides high throughput, high bandwidth utilization, ...
Keywords: Fairness, Metropolitan area network, Optical burst switching, QoS, Ring
- 12 [Commercial models for IP quality of service interconnect](#)
 B. Briscoe, S. Rost
 April 2005 **BT Technology Journal** Volume 23 Issue 2
Publisher: Kluwer Academic Publishers
Bibliometrics: Downloads (6 Weeks): n/a, Downloads (12 Months): n/a, Downloads (Overall): n/a, Citation Count: 0
 Interconnection of IP QoS capabilities between networks releases considerable value. In this paper we show how this value will be realised. We give technical and economic arguments for why QoS will be provided in core and backbone networks as a bulk ...
- 13 [Leveraging on-chip networks for data cache migration in chip multiprocessors](#)
 Neel Elsay, Li-Shuan Peh, Li Shang
 October 2008 **PACT '08: Proceedings of the 17th international conference on Parallel architectures and compilation techniques**

Publisher: ACM  [Request Permissions](#)

Full text available  [PDF](#) (414.08 KB)

Bibliometrics Downloads (6 Weeks): 6, Downloads (12 Months): 51, Downloads (Overall): 252, Citation Count: 4

Recently, chip multiprocessors (CMPs) have arisen as the *de facto* design for modern high-performance process with increasing core counts. An important property of CMPs is that remote, but on-chip, L2 cache accesses are costly than off-chip ...

Keywords: CMP, chip-multiprocessor, interconnection network, migration, network-driven computing

14 [A Fokker-Planck equation method predicting Buffer occupancy in a single queue](#)

[Dawn S. L. Doley](#), [Costas C. Constantinou](#), [Steven F. Graybe](#)

June 2007

Computer Networks: The International Journal of Computer and Telecommunications Networking Volume 51 Issue 5

Publisher: Elsevier North-Holland, Inc.

Bibliometrics Downloads (6 Weeks): n/a, Downloads (12 Months): n/a, Downloads (Overall): n/a, Citation Count

The Virtual Predictor Buffer (VIPER) algorithm is a novel algorithm for performing online prediction of the buffer space requirement of each competing traffic stream. It accomplishes its task through the employment of a theoretical, infinite capacity, ...

Keywords: Dynamic buffer allocation, Queue management, Queueing theory

15 [Sizing router buffers](#)

 [Osimo Appavouler](#), [Isaac Kostassev](#), [Nick McKeown](#)

August 2004 **SIGCOMM '04: Proceedings of the 2004 conference on Applications, technologies, architectures, and protocols for computer communications**

Publisher: ACM  [Request Permissions](#)

Full text available  [PDF](#) (294.61 KB)

Bibliometrics Downloads (6 Weeks): 20, Downloads (12 Months): 227, Downloads (Overall): 1422, Citation Count

All Internet routers contain buffers to hold packets during times of congestion. Today, the size of the buffers is determined by the dynamics of TCP's congestion control algorithm. In particular, the goal is to make sure that a link is congested, ...

Keywords: TCP, bandwidth delay product, buffer size, internet router

Also published in:

August 2004 **SIGCOMM Computer Communication Review** Volume 34 Issue 4

16 [Exploring High-Dimensional Topologies for NoC Design Through an Integrated Analysis and Synthesis Framework](#)

[Francisco Gilibert](#), [Simone Medardoni](#), [Davide Bertozzi](#), [Luca Benini](#), [Maria Engracia Gomez](#), [Pedro Lopez](#), [José Qui](#)

April 2008

NCS '08: Proceedings of the Second ACM/IEEE International Symposium on Networks-on-Chip

Publisher: IEEE Computer Society

Full text available  [PDF](#) (444.86 KB)

Bibliometrics Downloads (6 Weeks): 1, Downloads (12 Months): 12, Downloads (Overall): 92, Citation Count: 7

Networks-on-chip (NoCs) address the challenge to provide scalable communication bandwidth to tiled architect in a power-efficient fashion. The 2-D mesh is currently the most popular regular topology used for on-chip netw in tile-based architectures, ...

Keywords: Interconnection networks, networks on chip, chip design, topologies

17 [Contention Resolution for Transmission Control Protocol over Optical Burst-Switching Networks Using Random Segment Dropping](#)

Walter Abdallah, Mohamed Hamdi, Noureddine Boucraie, Mohammed S. Chabada

April 2010 **Simulation** Volume 88 Issue 4

Publisher: Society for Computer Simulation International

Bibliometrics: Downloads (6 Weeks): n/a, Downloads (12 Months): n/a, Downloads (Overall): n/a, Citation Count:

Optical burst switching (OBS) is a promising method for data transfer in photonic networks based on a Wavelength Division Multiplexing (WDM) technology. Transmission control protocol (TCP)-based applications generate the majority of data traffic in the ...


Keywords: Optical burst switching, congestion avoidance, modeling and simulation, optical delay lines, performance evaluation, random packet dropping, virtual optical memory

18 JumboGen: dynamic jumbo frame generation for network performance scalability

Daxin Sathya, Yingxin Jiang, Aijun Siliang, Christian Pommerehne

October 2007 **SIGCOMM Computer Communication Review** Volume 37 Issue 5

Publisher: ACM

Full text available  (376.22 KB)

Bibliometrics: Downloads (6 Weeks): 2, Downloads (12 Months): 13, Downloads (Overall): 165, Citation Count: 2

Network line speeds have increased at a significant rate. Unfortunately, network performance has not been able to keep pace with increases in line speed. This is due to the majority of packets being less than or equal to 100 bytes in addition to network ...

Keywords: JumboGen, dynamic jumbo frames, router scalability

19 Quality-of-service and error control techniques for mesh-based network-on-chip architectures

Rajwan Venkatesh, Nishan Ranasinghe, Karan S. Chaitin

January 2005 **Integration, the VLSI Journal** Volume 38 Issue 3

Publisher: Elsevier Science Publishers B. V.

Bibliometrics: Downloads (6 Weeks): n/a, Downloads (12 Months): n/a, Downloads (Overall): n/a, Citation Count:

Network-on-a-chip (NoC) has been proposed as a solution for addressing the design challenges of future high-performance system-on-chip architectures in the nanoscale regime. Many real-time applications require input data that arrives with low delay jitter ...

Keywords: Network-on-chip, Performance, Power consumption, Quality-of-service, Error-control

20 Dynamic cache clustering for chip multiprocessors

Muhammad Hameed, Sangwon Cho, Rami Melhem

June 2009 **ICS '09: Proceedings of the 23rd International conference on Supercomputing**

Publisher: ACM 

Full text available  (7.86 MB)

Bibliometrics: Downloads (6 Weeks): 6, Downloads (12 Months): 54, Downloads (Overall): 277, Citation Count: 3

This paper proposes DCC (Dynamic Cache Clustering), a novel distributed cache management scheme for large scale chip multiprocessors. Using DCC, a per-core cache cluster is comprised of a number of L2 cache banks an cache clusters are constructed, expanded, ...

Keywords: chip multiprocessor (cmp), non-uniform cache architecture (nuca)

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